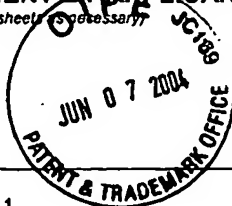


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STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/749927
Filing Date	December 29, 2003
First Named Inventor	Franca-Neto, Luiz
Group Art Unit	Unknown
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 884.B65US1

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Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.	T ²
/TW/		FRANCA-NETO, L. M., "Enabling High-Performance Mixed-Signal System-on-a-Chip (SoC) in High Performance Logic CMOS Technology", <u>2002 Symposium on VLSI Circuits Digest of Technical Papers</u> , 164-167	
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/Ted Wang/

DATE CONSIDERED

12/12/2007

Substitute Disclosure Statement Form (PTO-1449)

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